Title: INTEGRATED CIRCUIT HAVING NONVOLATILE DATA STORAGE CIRCUIT

Inventor: Wataru YOKOZEKI Application No. New Docket No. 108066-00097

FIG. 1

Prior Art

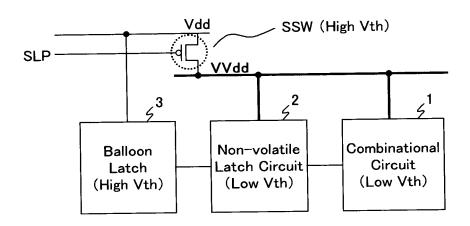
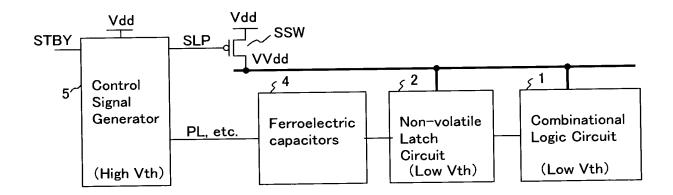


FIG. 2



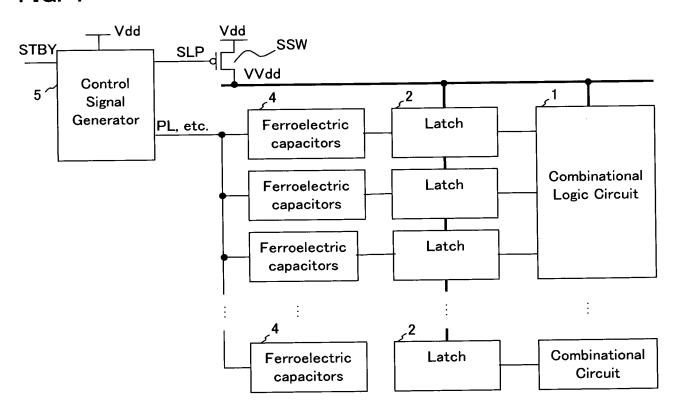
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Docket No. 108066-00097

SLPx

FIG. 3 Vdd Vdd SLP **STBY** SSWP VVdd ٤1 ς**2** 4ے Control 5 Signal Generator Combinational Non-volatile Ferroelectric PL, etc. Logic Circuit Latch capacitors Circuit (Low Vth) (Low Vth) (High Vth) VVss SSWN

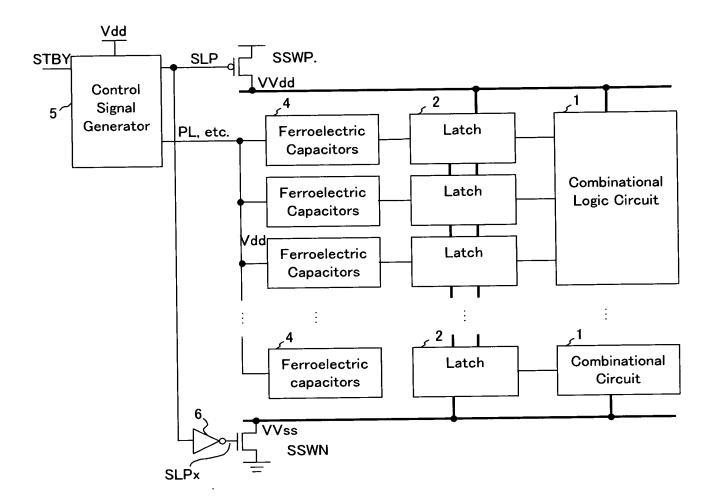
FIG. 4



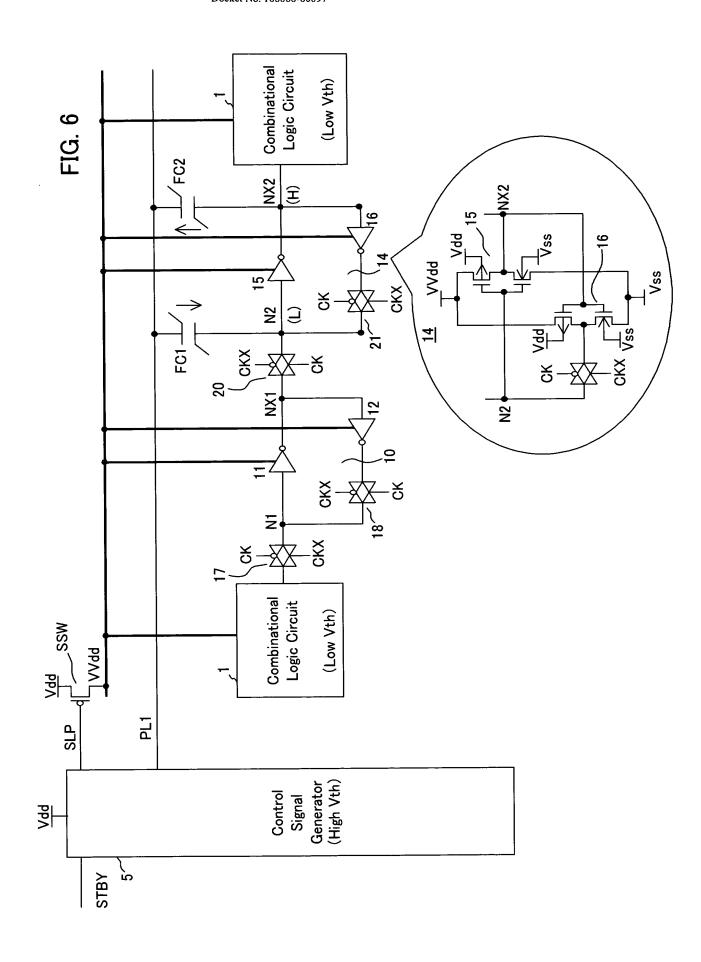
Title: INTEGRATED CIRCUIT HAVING NONVOLATILE DATA STORAGE CIRCUIT Inventor: Wataru YOKOZEKI

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FIG. 5



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FIG. 7

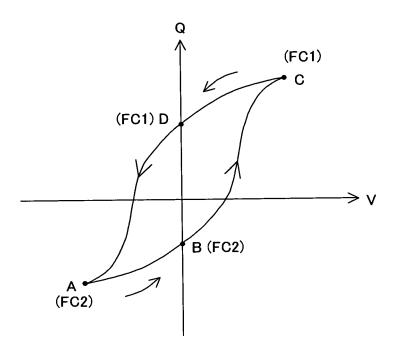
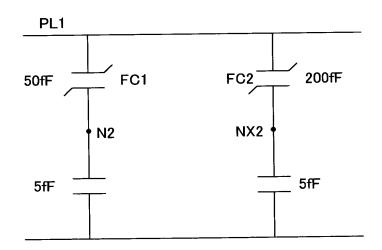
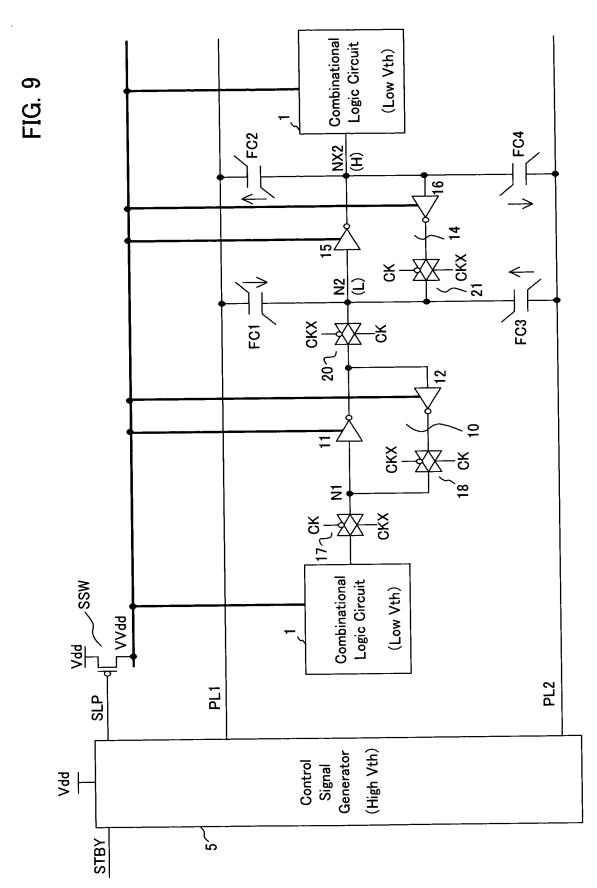


FIG. 8



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FIG. 10

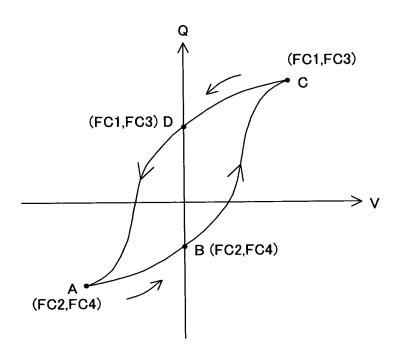
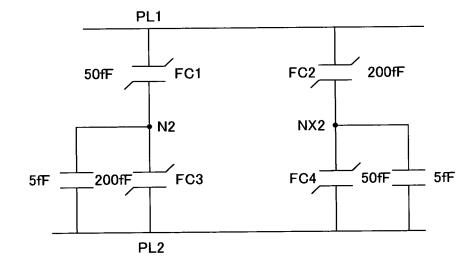
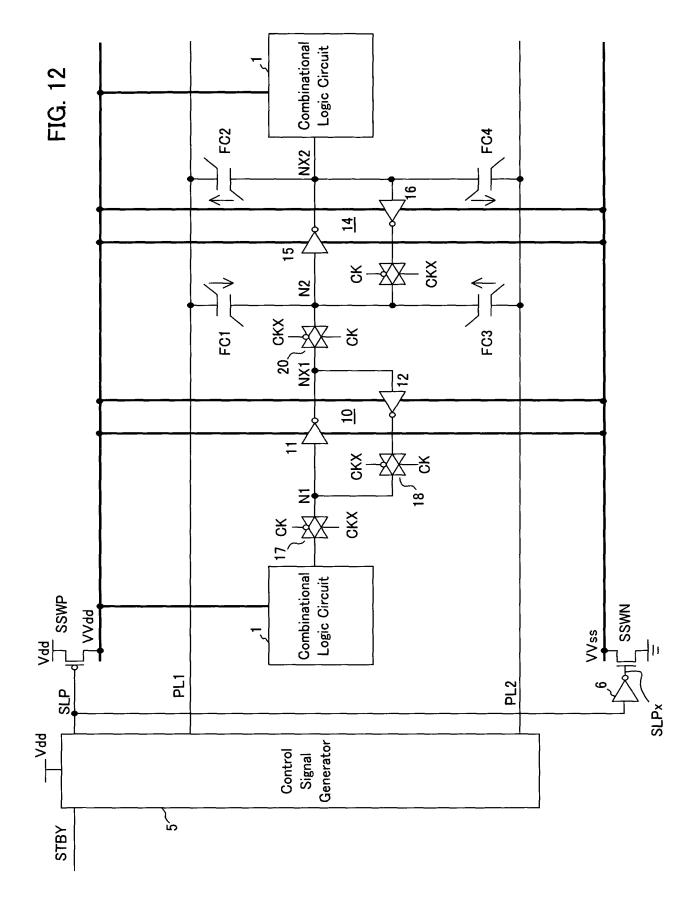


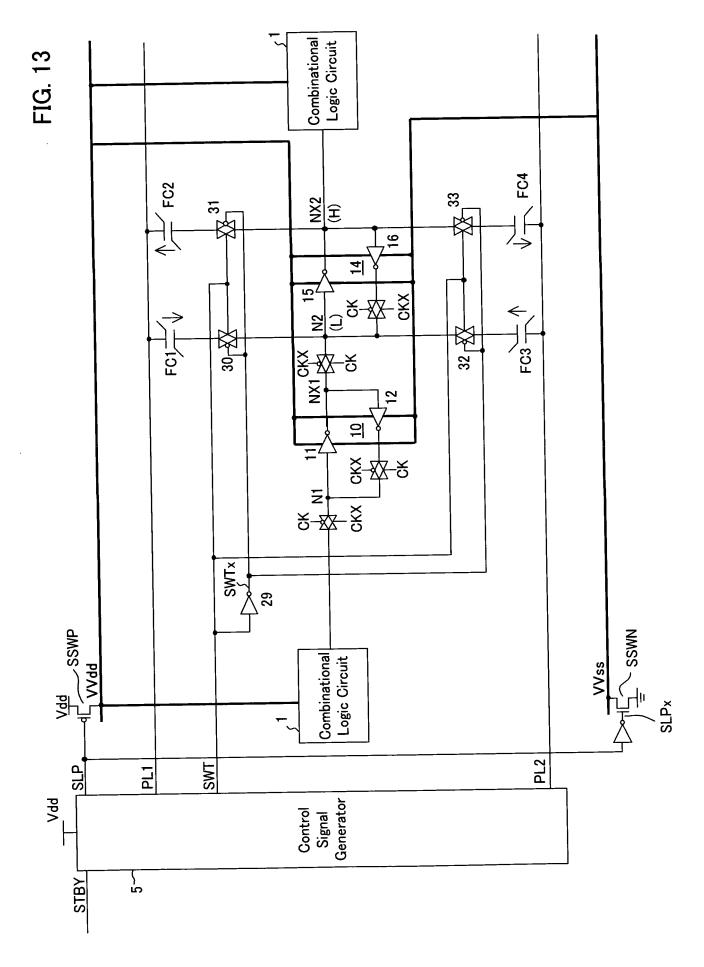
FIG. 11

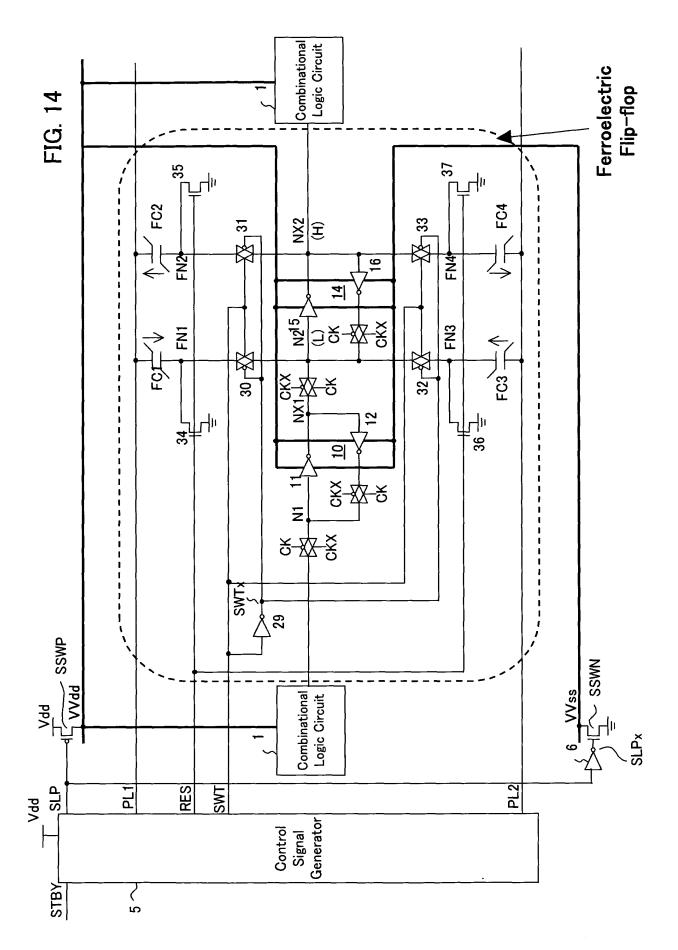


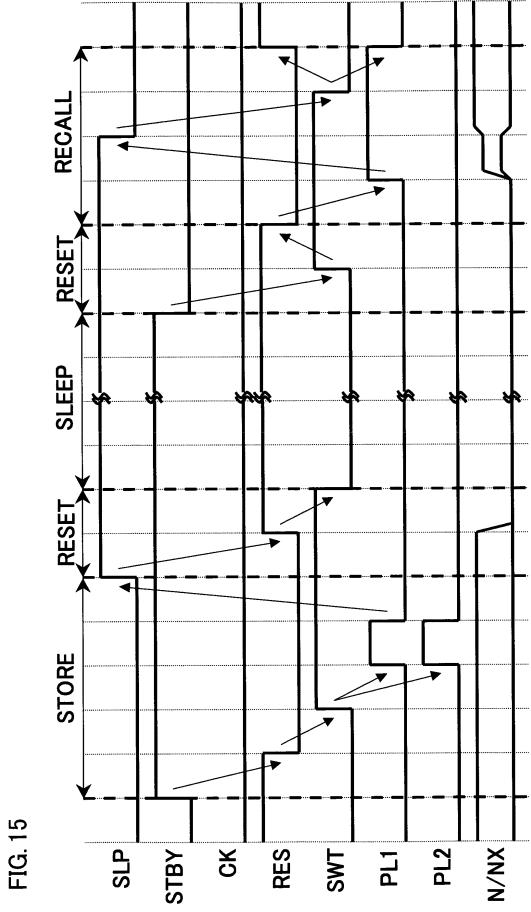
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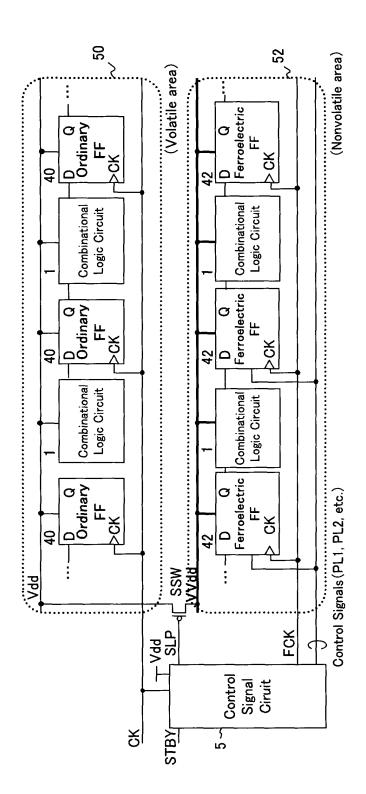


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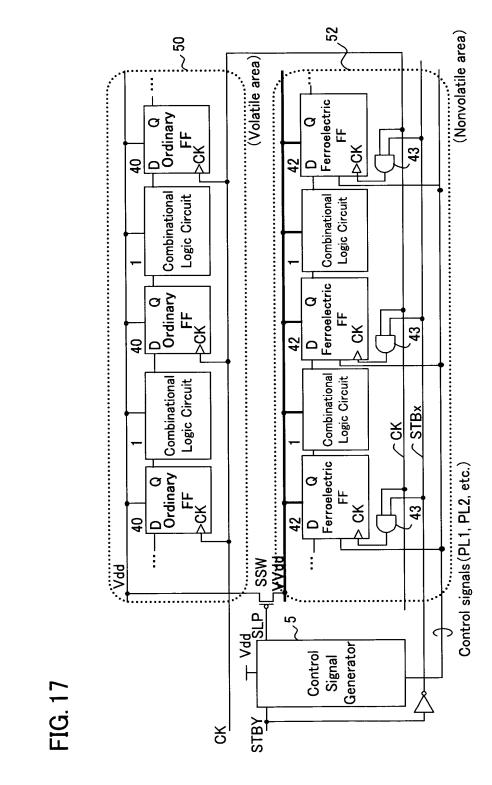
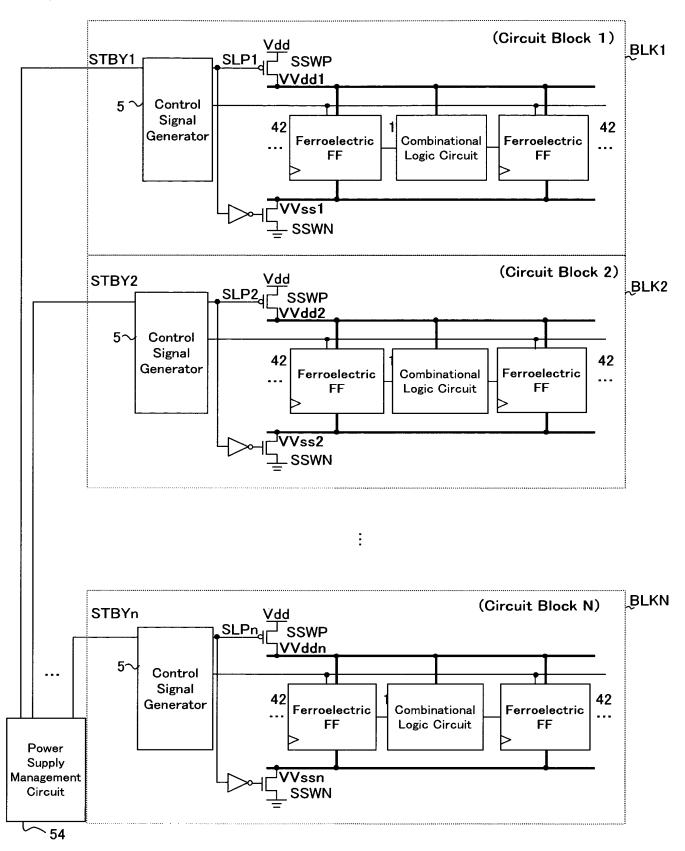


FIG. 17

NONVOLATILE DATA STORAGE CIRCUIT

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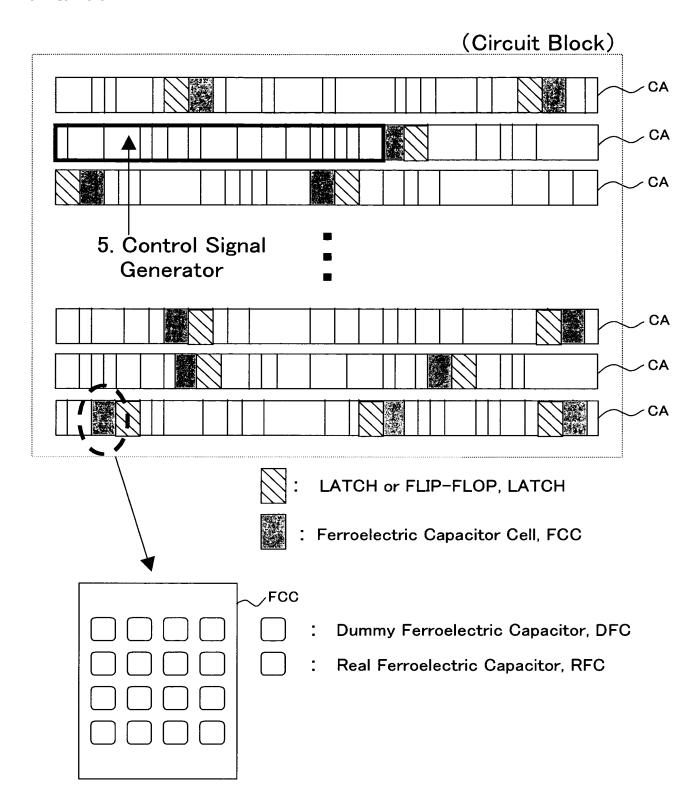
FIG. 18



NONVOLATILE DATA STORAGE CIRCUIT Inventor: Wataru YOKOZEKI

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FIG. 19



NONVOLATILE DATA STORAGE CIRCUIT Inventor: Wataru YOKOZEKI

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FIG. 20

